

What is claimed is:

1. A 0.75-power computing apparatus, wherein a multiplication between an infinity and zero which leads its operation result to an indefinite value is inhibited.

2. A 0.75-power computing apparatus, comprising: inverse number computing means for computing an inverse number of an input value; first -0.5-power computing means for computing the -0.5-power of said input value; multiplication means for computing a product between a computed result of said inverse
10 number computing means and a computed result of said -0.5-power computing means; and second -0.5-power computing means for computing the -0.5-power of a computed result of said multiplication means.

3. The 0.75-power computing apparatus according to claim 2,
15 being used for a coding method employing the computation of the 0.75-power.

4. The 0.75-power computing apparatus according to claim 3,
being used for an audio signal quantization device quantizing a transformed audio signal by using the computation of the
20 0.75-power.

5. A 0.75-power computing method, wherein a multiplication between an infinity and zero which leads its operation result to an indefinite value is inhibited.

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6. A 0.75-power computing method, comprising: a first step for computing an inverse number of an input value; a second step for computing the -0.5-power of said input value; a third step for computing a product between a computed result at said first
5 step and a computed result at said second step; and a fourth step for computing the -0.5-power of a computed result at said third step.

7. The 0.75-power computing method according to claim 6, being used for a coding method employing the computation of the
10 0.75-power.

8. The 0.75-power computing method according to claim 7, being used for an audio signal quantization device quantizing a transformed audio signal by using the computation of the 0.75-power.

15 9. A program that is executed on a computer, wherein a multiplication between an infinity and zero which leads its operation result to an indefinite value is inhibited.

10. A program that is executed on a computer, comprising: a first step for computing an inverse number of an input value;
20 a second step for computing the -0.5-power of said input value; a third step for computing a product between a computed result at said first step and a computed result at said second step;

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and a fourth step for computing the -0.5 -power of a computed result at said third step.

11. The program according to claim 10, being used for a coding method employing the computation of the 0.75 -power.

- 5 12. The program according to claim 11, being used for an audio signal quantization device quantizing a transformed audio signal by using the computation of the 0.75 -power.

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